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Guest editorial: Nanotech 2006

This issue contains selected papers from *Nanotech 2006*, the NSTI nanotechnology conference and trade show, which was held May 7–11th 2006 in Boston, MA. A key part of the NSTI conference was once again the International Conference on Computational Nanoscience and Nanotechnology (ICCN). ICCN included symposia covering everything from molecular simulation of materials and biological systems to device modelling. The papers in this special issue are a representation of this breadth. They provide insights into new method

developments such as the linear scaling DFT method ONETEP, as well as application studies investigating various issues critical to nanoscale device design, including MOSFETs, biosensors, and nanofluidic devices.

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